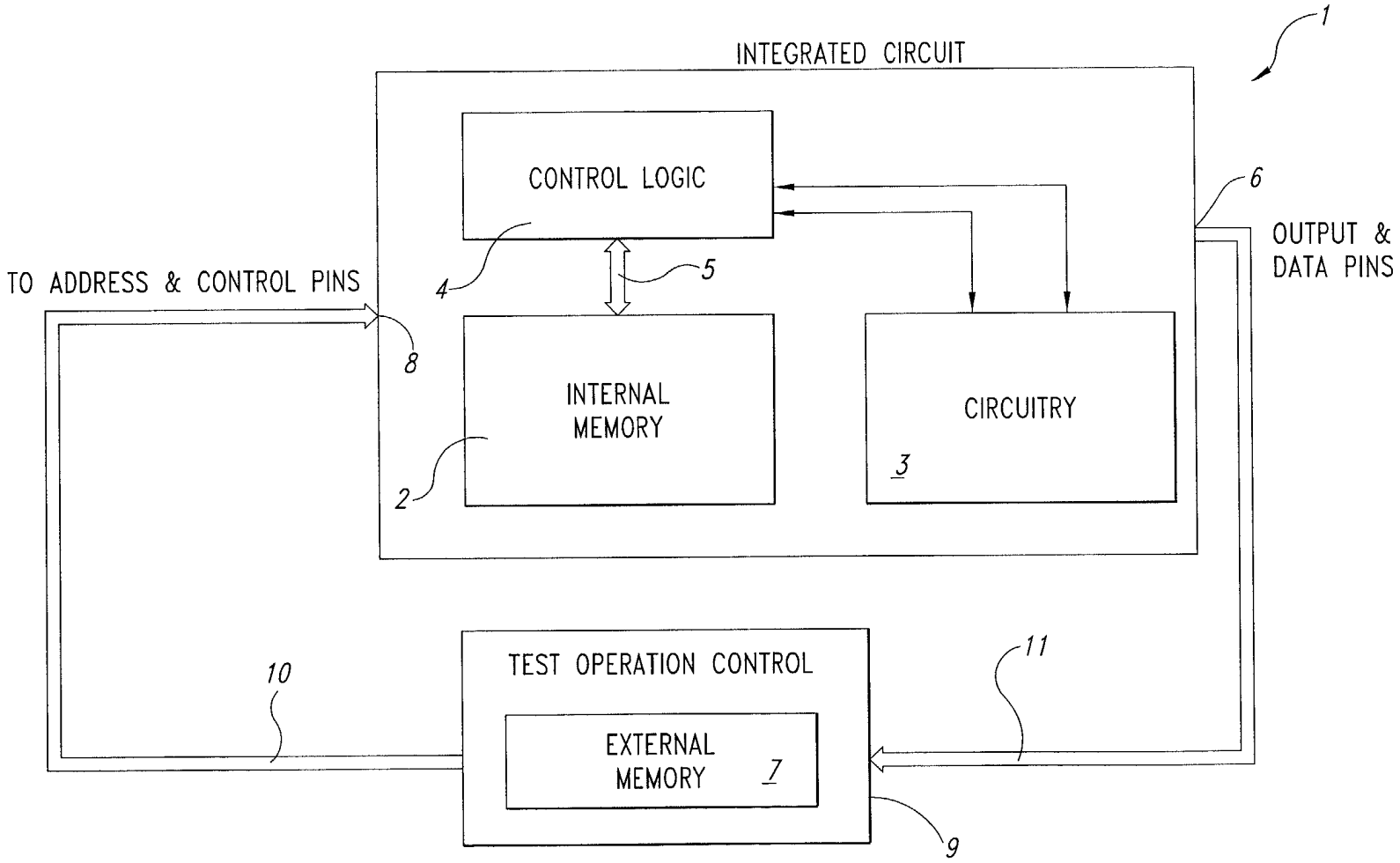
1
INTEGRATED CIRCUIT
CONTROL LOGIC
6
OUTPUT &
DATA PINS
TO ADDRESS & CONTROL PINS
4
5
8
INTERNAL
MEMORY
CIRCUITRY
3
2
TEST OPERATION CONTROL
10
11
EXTERNAL
MEMORY
7
9